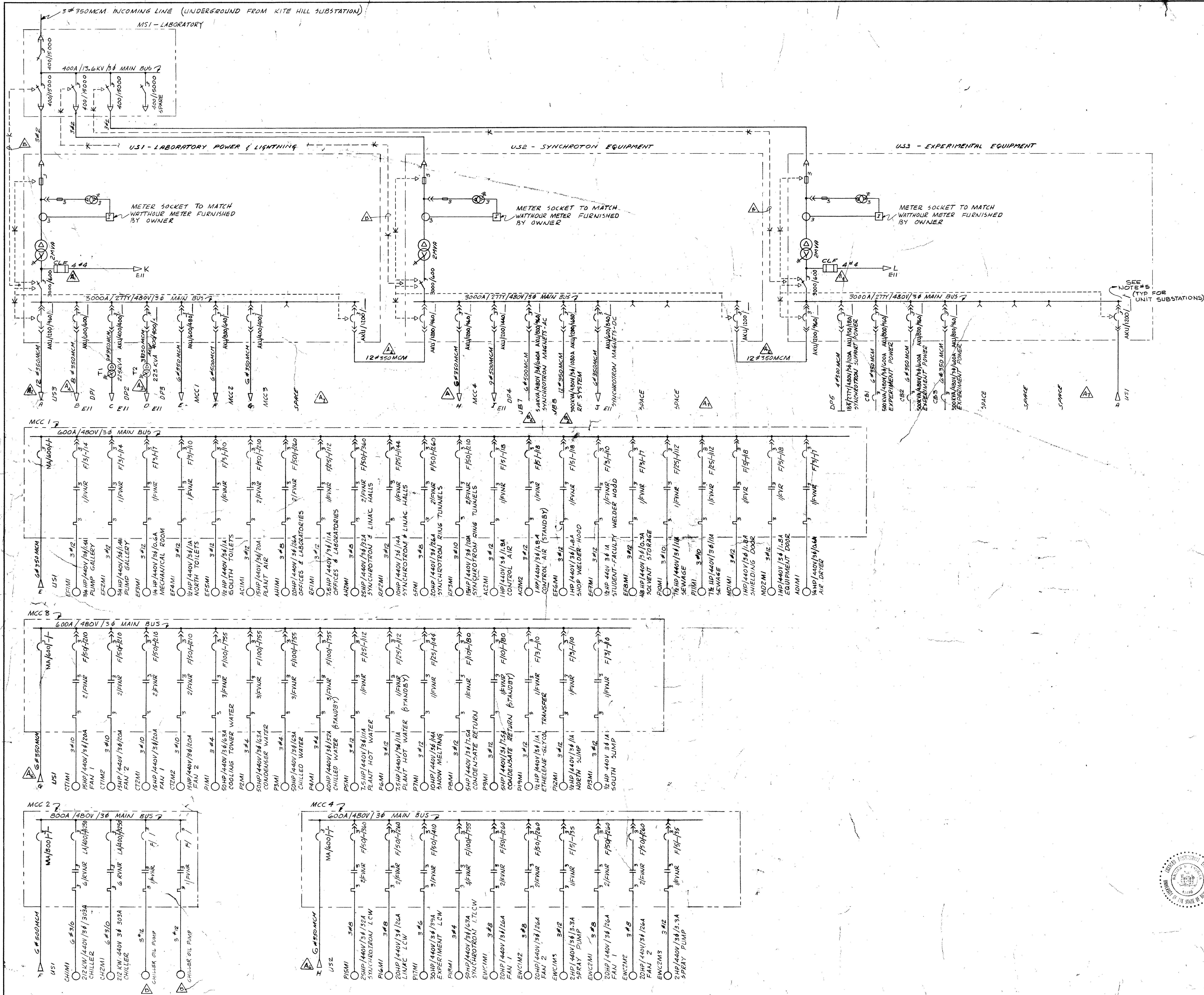




- NOTES:
1. ALL CIRCUITS SHALL BE PHASED OUT AND IDENTIFIED IN ACCORDANCE WITH OWNER'S STANDARDS.
 2. ALL TRANSFORMER TAPS SHALL BE SET TO MAINTAIN THE FOLLOWING MAXIMUM AND MINIMUM VOLTAGES, AT THE UTILIZATION EQUIPMENT, UNDER ALL STEADY STATE CONDITIONS:
- | SYSTEM | VOLTAGES | MINIMUM ALLOWABLE |
|-----------|-----------|-------------------|
| NORMAL | 277Y/480V | 277Y/480V |
| 277Y/480V | 277Y/480V | 250Y/434V |
| 120Y/208V | 125Y/217V | 114Y/197V |
3. MANUFACTURERS OF THE UNIT SUB-STATIONS SHALL FURNISH THE PRIMARY HIGH VOLTAGE FUSES TO COORDINATE WITH THE LOW VOLTAGE SWITCHGEAR FUSED CIRCUIT BREAKERS.
 4. MANUFACTURERS OF THE UNIT SUB-STATIONS SHALL PROVIDE CURRENT LIMIT FUSES IN THE FUSED CIRCUIT BREAKERS THAT WILL LIMIT THE MAXIMUM FAULT CURRENT TO A MAXIMUM OF 25 000 RMS SYMMETRICAL AMPERES.
 5. MANUFACTURE OF UNIT SUBSTATION SHALL PROVIDE CIRCUIT BREAKER INSTANTANEOUS TRIP TO COORDINATE WITH FUSE

AS-BUILT CORRECTIONS	1-20-61	RPE
1) ADDED FUSES FOR EMERGENCY CIRCUITS		
2) BUS TIES CHG. FROM 9" #350 MCM		
3) US1-D CHG FROM 6" #350 MCM TO 6" #350 MCM		
4) US1-B CHG FROM 6" #350 MCM TO 6" #350 MCM		
5) ADDED NOTE #5		
6) REVISED TO CONFORM WITH E-14		
7) DELETED 1 SPACE FROM US1 & US2 & US3		
LTR	REVISIONS	DATE
	BY	APPD

IO GEV CORNELL
SYNCHROTRON LABORATORY
CORNELL UNIVERSITY
ITHACA NEW YORK

POWER
SINGLE LINE DIAGRAM
SHEET 1 OF 2

WILLIAM M. BROBECK AND ASSOCIATES
IAN MACKINLAY, AIA ARCHITECT
PREGNOFF & MATHEU STRUCTURAL ENGR.
JACOBS ASSOCIATES CONSTRUCTION ENGR.
950 GILMAN ST. BERKELEY, CALIF. 94710
TELEPHONE 415 - 524 - 8664

DR. CUTAIA	CHK. J. CUTAIA	SUPV. J. CUTAIA	SCALE 1/8"=1'-0"
RELEASED	APPROVED	SHEET 84 OF 102	DRAWING NO.
T.H. Long	LAB NUCLEAR STUDIES		
DATE NOV 18, 1963	DATE JAN 15, 1966		