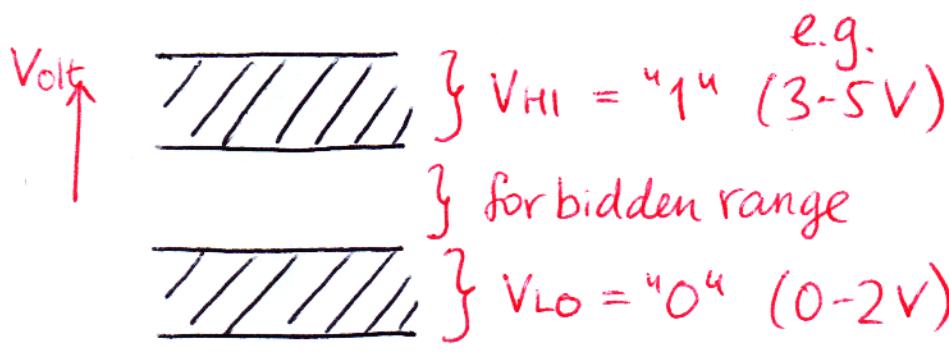


Lecture 23

Analog signals info represented by continuously varying I & V

- sensitive to drifts, noise, etc.
- 1 part in 10^6 accuracy is difficult

Digital signals

positive logic:

$$V_{HI} > V_{LO}$$

$$\text{"1"} \nearrow \text{"0"}$$

negative logic

- only two discrete voltage ranges "0" and "1"
FALSE and TRUE
- the info typically is encoded "computer"
- digital circuits perform logical op (on "0" and "1's")
- digital signals can be recovered exactly
- e.g. 1 part in 10^{13} accuracy is easy

	analog	digital
original signal (vs. time)		
signal + noise		
recovered?	No	
arbitrary shapes?		Yes (e.g. Schmitt trigger)

② digital repr.
of signal

still analog

Digital representation of info

e.g. temperature $43.124\dots {}^{\circ}\text{F}$

① truncate to some precision, e.g. $1{}^{\circ}\text{F}$

$$43_{(10)} = 4 \times 10^1 + 3 \times 10^0 \leftarrow \text{decimal representation}$$

\nwarrow decimal base

② need binary representation. Several options:

a) binary numbers

$$43_{(10)} = 1 \times 2^5 + 0 \times 2^4 + 1 \times 2^3 + 0 \times 2^2 + 1 \times 2^1 + 1 \times 2^0$$

$$43_{(10)} = 101011_2 \leftarrow \begin{matrix} \uparrow\uparrow \\ \text{bits} \end{matrix} \text{binary base}$$

- each bit = binary-valued variable
- byte = 8 bits

b) use binary codes (lookup tables)

(3)

Example 1. Binary-coded decimal

$3360_{10} \rightarrow 0011\ 0011\ 0110\ 0000_{BCD}$

each digit 0-9 is represented by 4-bit binary

Example 2. Gray code - used to represent successive numbers

decimal	binary (3-bit)	Gray code	
0	000	000	only 1 bit changes
1	001	001	on successive number
2	010	011	increments
3	011	010	
4	100 ² glitch	110	pro: avoids "glitches"
5	101	111	when multiple bits
6	110	101	need to change
7	111 ² possible	100	simultaneously
	1000		

Example 3. ASCII - american standard code for info inter-change

"a" = 1100001 "A" = 1000001

"b" = 1100010 "B" = 1000010

"c" = 1100011 "C" = 1000011

7-bit to represent "printable" characters

$75_{10} \rightarrow \underline{\quad}_2 ?$

64 - 32 - 16 - 8 - 4 - 2 - 1

1(1) 0 0 1(3) 0 1(1) 1

$\Rightarrow 1001011_2$

{ binary to gray? $101101_2 \rightarrow \underline{\quad}_{gray} ?$

{ MSB 1 0 0 1 1 0 0

 > 1 1 1 0 1 1

$\Rightarrow 111011_{gray}$

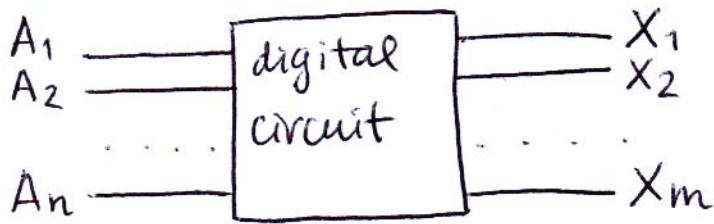
45_{10}

Types of digital circuits

(4)

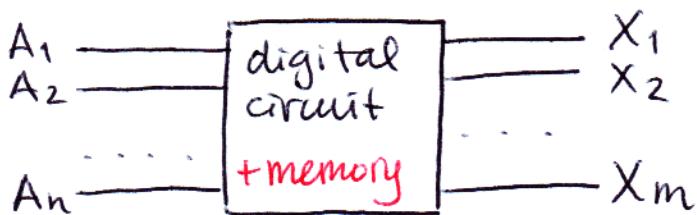
In general, a digital circuit has

n inputs $A_i, i=1 \dots n$
 m outputs $X_j, j=1 \dots m$



- If output depends only on present inputs

$$X_j = f_j(A_1, A_2, \dots, A_n) \Rightarrow \text{"combinational logic"}$$



- If output depends both on present and past inputs (thru memory)

$$X_j = f_j(A_1 \dots A_n; \underbrace{M_1 \dots M_e}_{\text{memory (flip-flops)}}) \Rightarrow \text{"sequential logic"}$$

Truth table

(lookup table) - list all input combinations and corresponding outputs ("0" and "1"'s only)

size: $2^n \times m$

e.g. 2 inputs + 1 output: $2^2 = 4$

5 inputs + 4 outputs: $2^5 \times 4 = 128$

16 inputs + 8 outputs: $8 \times 2^{16} = 524k$