

Lecture 36

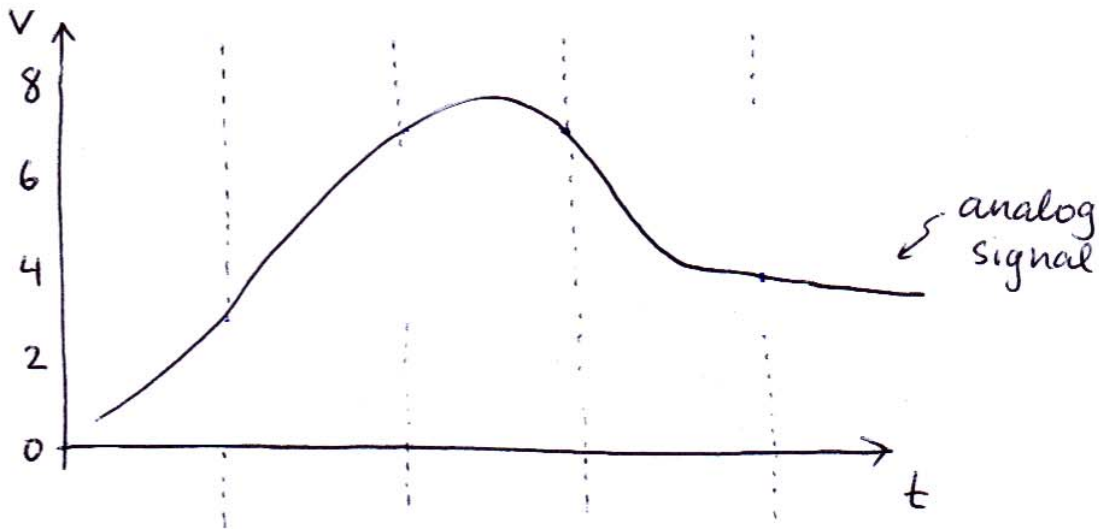
A/D and D/A converters

- 
- 



Digital representation of info

- 
- 



- both \_\_\_\_\_ and \_\_\_\_\_  
affect fidelity of representation

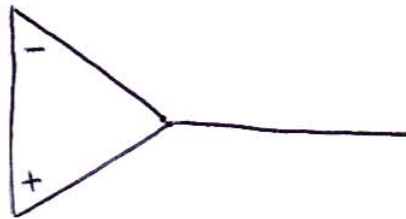
1) # bits

②

2) sampling rate

D/A conversion

recall summing circuit



Assume

Problems

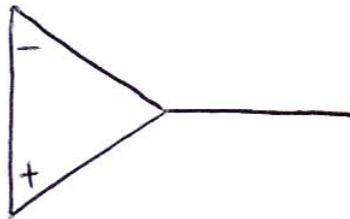
③

1)

2)

3)

R-2R ladder D/A converter



starting from LSB

④

- widely used

-